



Department of Electrical and Computer Engineering

ELE 305: Introduction to Electrical Engineering Final Exam – Fall 2016

Duration:	2 hours
Date: 16/12	2/2016
Start Time:	8:00 am

50/2

Dr. Harag Margossian

Name:	ID#:

INSTRUCTIONS:

- Answer each of the following questions in the space provided.
- You can use both sides of the sheets for answers.
- Solutions written outside this booklet will not be graded.
- This is a closed-book exam
- Programmable calculators and smart devices are not allowed.
- The number of points for each question is specified next to it.
- The total number of points is 100.

1	2	3	4	5	Total
/18	/24	/12	/22	/24	/100

Problem 1 - 18 pts

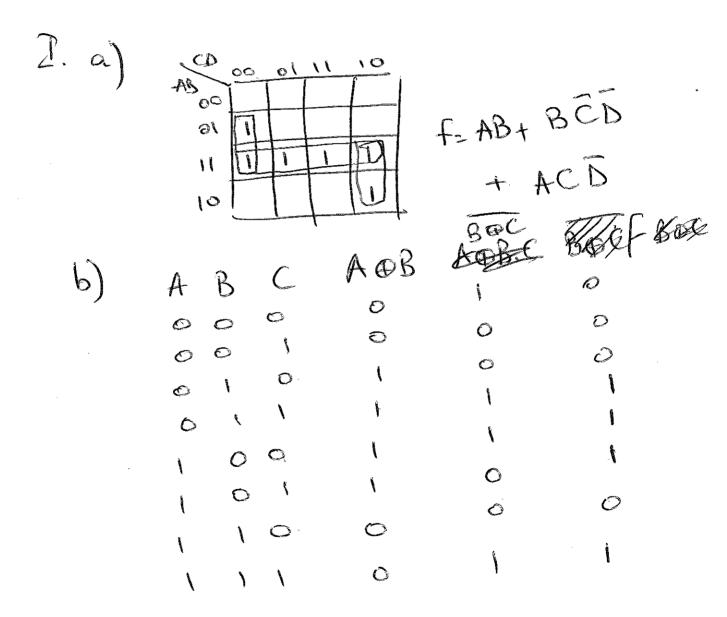
Answer the following questions:

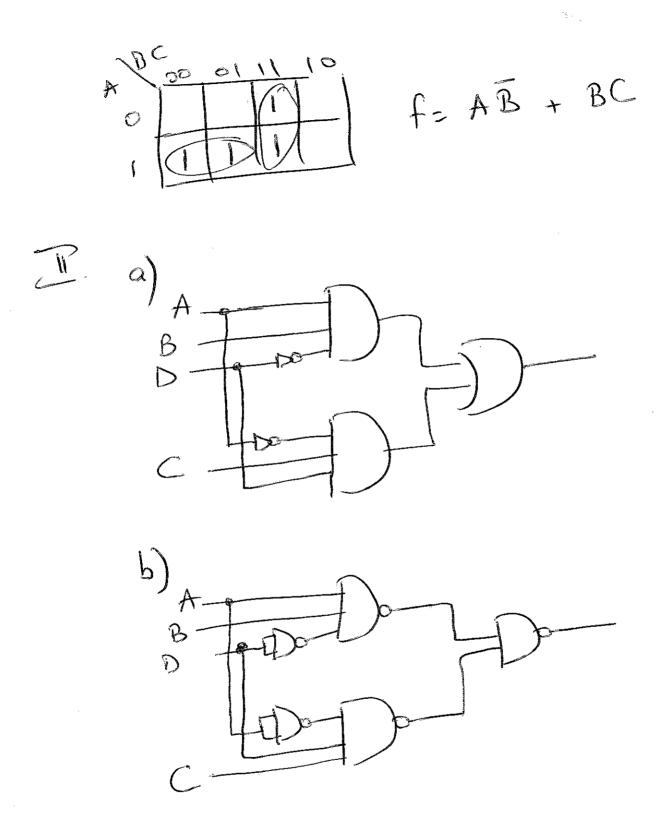
- I. The English alphabet (26 letters) is to be represented using binary numbers. How many <u>bits</u> are needed and how many terms remain unused if:
 - a. Only small (lower case) letters are to be represented
 - b. Both small and capital (upper case) letters are to be represented
- II. Let N=100...000 (n zeroes), M=111...111 (n ones). What are the decimal values of N and M (in terms of n) if:
 - a. The numbers follow the unsigned binary representation
 - b. The numbers follow the signed 2's complement representation
- III. Convert 30 and 27 into binary numbers. Using 6-bit 2's complements, compute 30+27 and 30-27. Convert the results back to decimal and verify that they are correct.

Problem 2 - 24 pts

Solver for the following logic circuit problems:

- I. Build the karnaugh map and write the minimum SOP expressions for the following:
 - a. $F = \overline{A}B\overline{C}\overline{D} + AB\overline{C}\overline{D} + AB\overline{C}D + ABCD + ABC\overline{D} + A\overline{B}C\overline{D}$
 - b. $F = (A \oplus B) \cdot C + A \cdot (\overline{B \oplus C})$
- II. Draw the logic circuit of the expression: $F = AB\overline{D} + \overline{A}CD$
 - a. Using AND, OR and NOT gates
 - b. Using the minimum number of (only) NAND gates

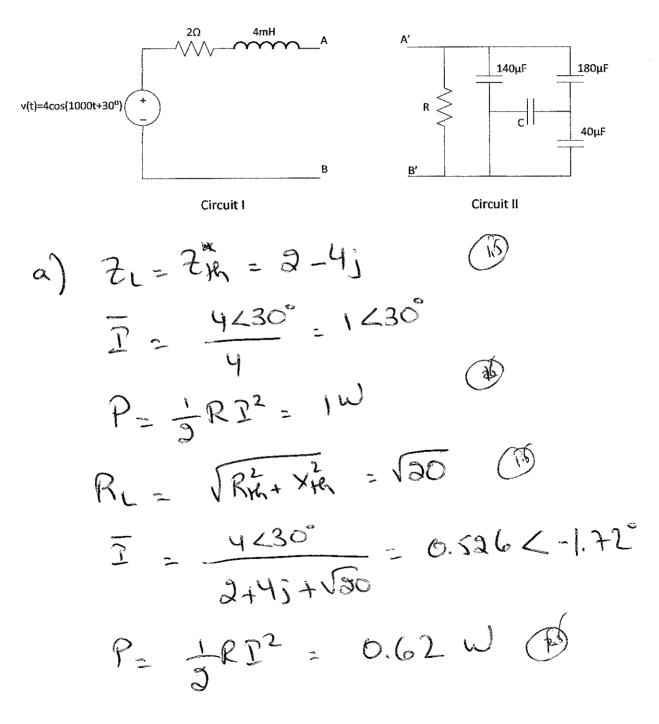




Problem 3 - 12 pts

Consider Circuits 1&II shown below.

- a) What is the maximum power that can be transferred to a complex load connected to Circuit I between nodes A and B? What if the load to be connected was just a resistance?
- b) If the load to be connected is Circuit II, what values for R and C should be chosen to achieve maximum power transfer?

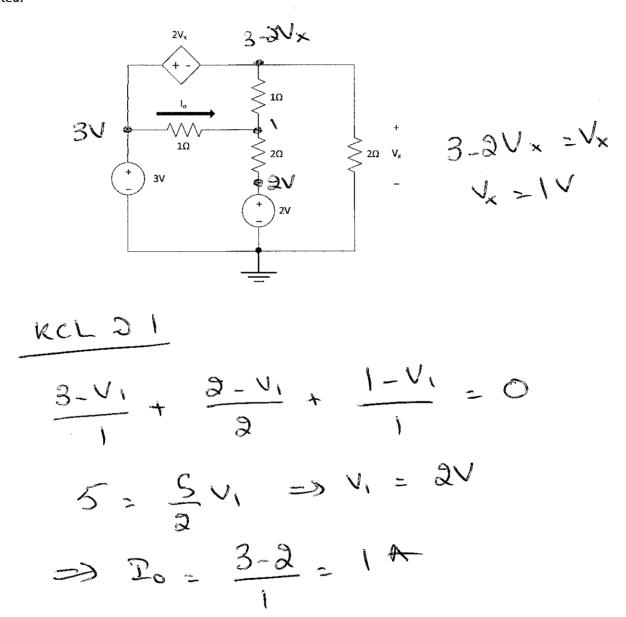


Page 7 of 13

b)
$$\frac{1}{2} = \frac{1}{2} + \frac{1}{2} = \frac{1}{R} + j\omega C'$$
 $\frac{1}{2-47} = \frac{1}{R} + \frac{1}{2C} = \frac{1}{R} + j\omega C'$
 $\frac{1}{2} = \frac{1}{0.1} = 10 \text{ SL}$
 $\frac{1}{3} = \frac{1}{0.1} = \frac{1}{0.1} = 10 \text{ SL}$
 $\frac{1}{3} = \frac{1}{0.1} = \frac{1}{0.1} = 10 \text{ SL}$
 $\frac{1}{3} = \frac{1}{0.1} = 10$

Problem 4 - 22 pts

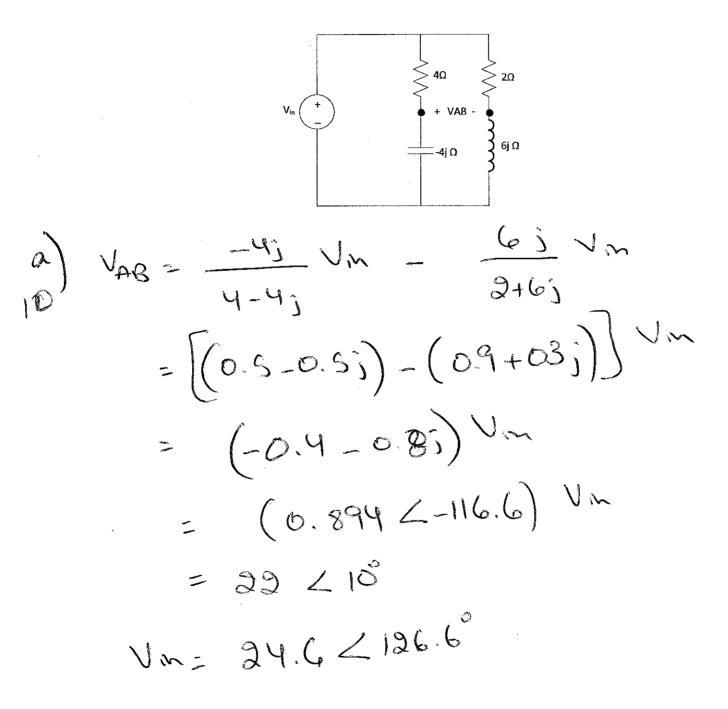
Find the current l_0 using <u>nodal analysis</u> for the network shown below, with the ground connected as illustrated.



Problem 5 - 24 pts

Consider the network shown below:

- a) If VAB=22<10° V, calculate the voltage Vin.
- b) Calculate the real and reactive power absorbed by the source Vin-
- c) What is the power factor of each of the two load branches on its own and of the two load branches together? Specify whether the power factors are leading or lagging.



Page 11 of 13

SCRATCH